

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comment s	Error Definition	Error ro rs
1	BRS	L3	1773	(substrate or (circuit adj board) and (semiconductor or ic or chip or processor or component) same (therm\$4 or cooling) and (spring same bottom)	USPA T	2004/09/2 7 14:52	-	-	0
2	BRS	L4	567	(substrate or (circuit adj board) and (semiconductor or ic or chip or processor or component) same (therm\$4 or cooling) and (heat adj sink) and (spring same bottom)	USPA T	2004/09/2 7 14:53	-	-	0